

INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE
Department of Electronics & Communication Engineering

Dated: 20/10/2021

ADVERTISEMENT TO FILL UP PROJECT POSITIONS*

Applications are invited from Indian nationals only for project position(s) as per the details given below for the research project under the principal investigator Prof. Arnab Datta, Department of Electronics & Communication Engineering, Indian Institute of Technology, Roorkee

1. Title of project: On Exploration of the Switching Behavior of Gapless Atomic Switch
 2. Sponsor of the project: SERB
 3. Project position(s) and number: JRF (1)
 4. Qualifications: First class M.Tech. with specialization in Micro/Nanoelectronics, VLSI, Nanoscience & Technology, Nanotechnology, Solid State Materials, Engineering Physics or M.Sc. (Physics/Electronics) with UGC or CSIR NET/GATE score in Electronics/Electronics & Communication Engineering/Physics. Strong understanding in the semiconductor device physics and experimental background in the same/closely related area are required (fabrication/characterization) as evidenced from the candidate's prior work(s). Familiarity with the CMOS process and characterization equipments and programming skill are essential. Candidates with M.Tech. / M.Sc. in the above specified areas without NET / GATE score can also apply, however they must possess at least 1 year research experience in the semiconductor device physics area as evidenced from publication record.
 5. Emoluments: *31,000/- per month
 6. Duration: 3 years
 7. Job description: Fabrication, characterization, data analysis and modeling of gapless atomic switch as per the project objective. **The selected candidate may get an opportunity for Ph.D. admission as per rule.** On campus hostel accommodation may be provided on selection
1. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply.
 2. Candidates desiring to appear for the Interview should submit their applications with the following documents (**combined in a single pdf file including cover letter**) to the Principal Investigator through email [arnab.datta@ece.iitr.ac.in] and produce them at the time of interview, if asked for:
 - Cover Letter substantiating courses taken in the areas of semiconductor devices, device fabrication and device/material characterization and also the experimental skills of material and device characterization as required for the project.
 - CV highlighting chronological discipline of degree/certificates obtained with CGPA/percentage of marks, experimental skills on device fabrication and characterization, familiarity with fabrication and characterization equipments, programming skill, dissertation work, and publications (if any)
 - Experience including research, industrial field and others (if any)
 - Scanned copies of the original degree/certificate(s) and experience certificate(s)
 3. Candidate shall provide original degree(s)/certificate(s) and experience certificate(s) at any stage if required for verification
 4. Preference will be given to SC/ST candidates on equal qualifications, experience and expertise
 5. Please note that no TA/DA is admissible for attending the interview.

The last date for application to be Emailed to the Principal Investigator is by **November 2, 2021 by 5 PM**

Tel: + 91- 1332-28 - 5464

Email: arnab.datta@ece.iitr.ac.in

' Approved '

[Arnab Datta]
 Name and signature
 of Principal Investigator

*To be uploaded on IIT Roorkee website and copy may be sent to appropriate addresses by PI for wider circulation.

अनुमोदक (सिर्फ) / Dean (SRIC)

Rjeu
 20-10-21

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